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3.8 Remarks Functional decomposition is the key technique in the memory-based logic synthesis. It efficiently represents a given Boolean function with reduced total amount of memory. Most Boolean functions do not have any functional decomposition [109].

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Shows logic design methods for index generation functions Introduces C-measure to specify the complexity of Boolean functions Introduces hash-based design methods that synthesize index-generation in IP address tables, packet filtering, terminal access controllers, memory patch circuits, virus scanning circuits, memory fault maps, and pattern ...

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Introduces hash-based design methods, which efficiently synthesize index generation functions by pairs of smaller memories and can be applied to IP address tables, packet filtering, terminal access controllers, memory patch circuits, virus scanning circuits, intrusion detection circuits, fault map of memories, code converters and pattern matching.

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Fast Logic Synthesis for RRAM-based In-Memory Computing using Majority-Inverter Graphs Saeideh Shirinzadeh*, Mathias Soeken*†‡, Pierre-Emmanuel Gaillardon§, Rolf Drechsler*† *Department of Mathematics and Computer Science, University of Bremen, Germany †Cyber-Physical Systems, DFKI GmbH, Bremen, Germany ‡Integrated Systems Laboratory, EPFL, Lausanne, Switzerland

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